

FIG. 1

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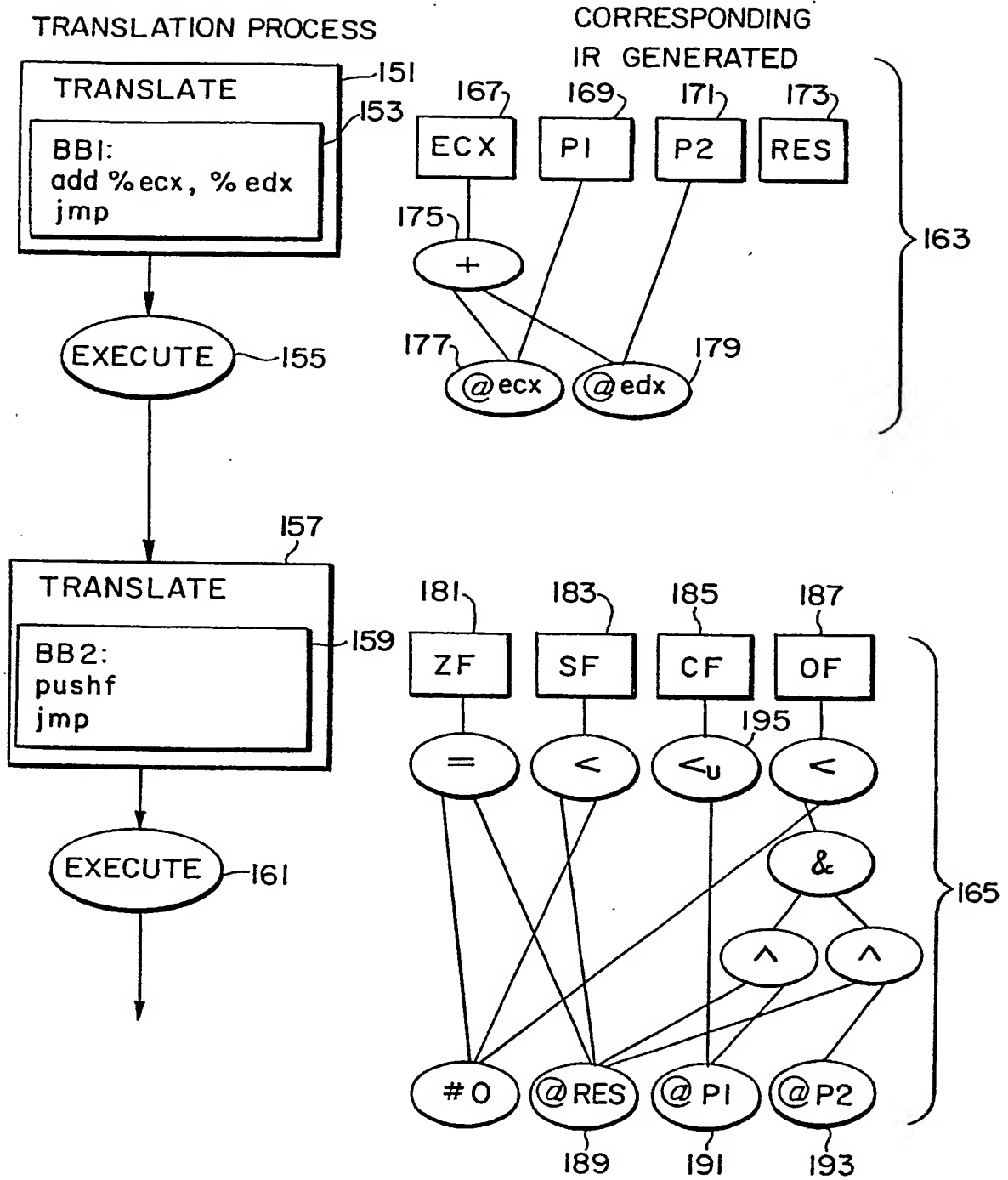


FIG. 2

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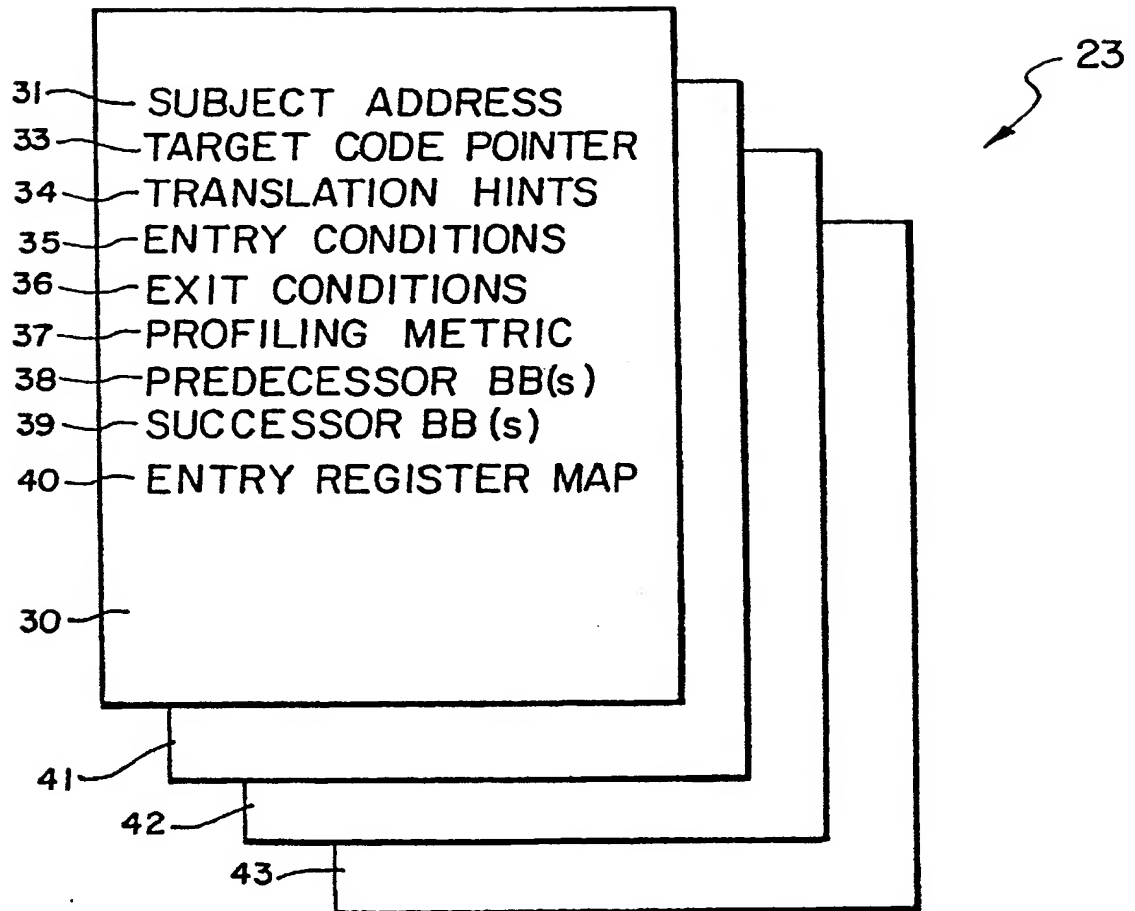


FIG. 3

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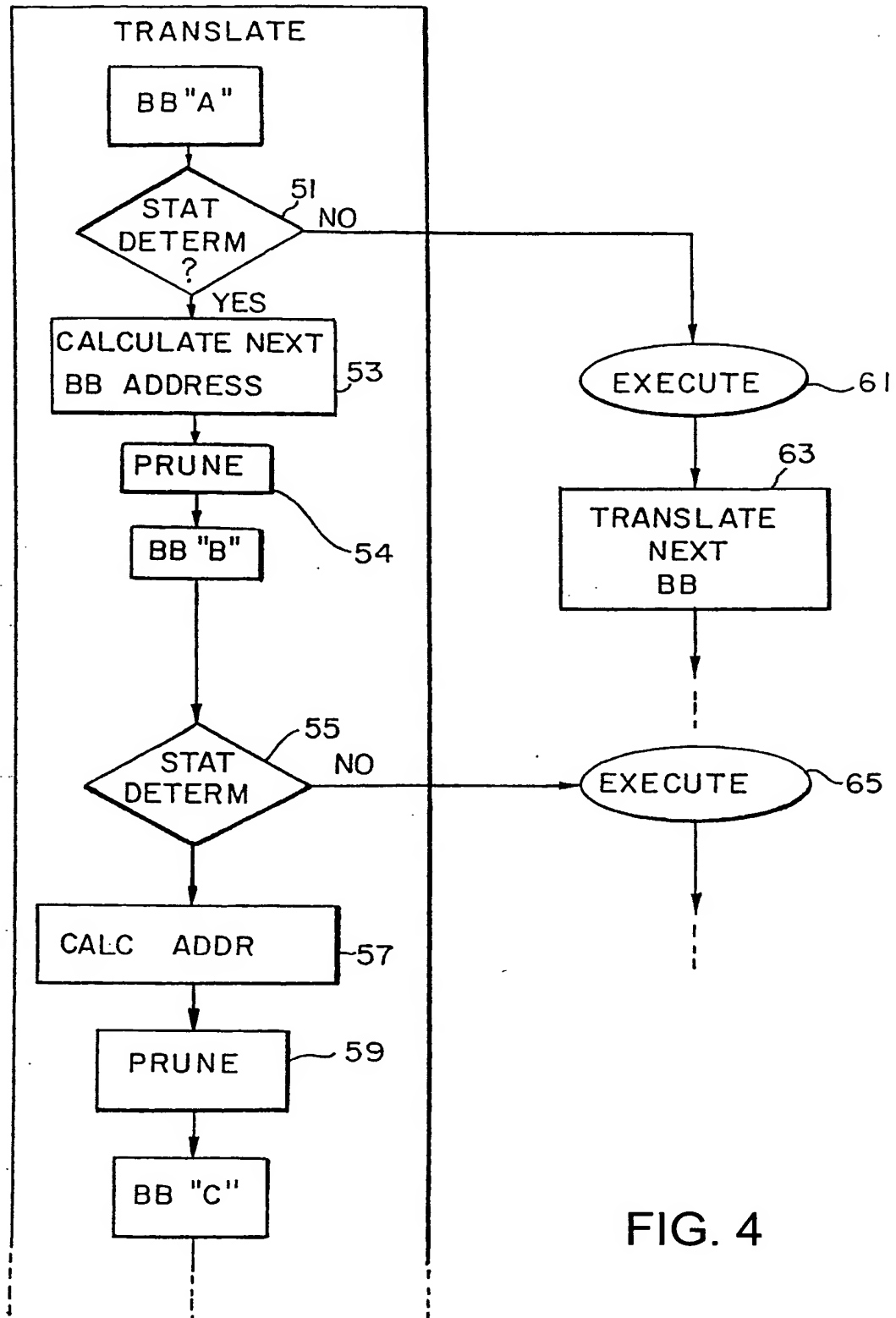


FIG. 4

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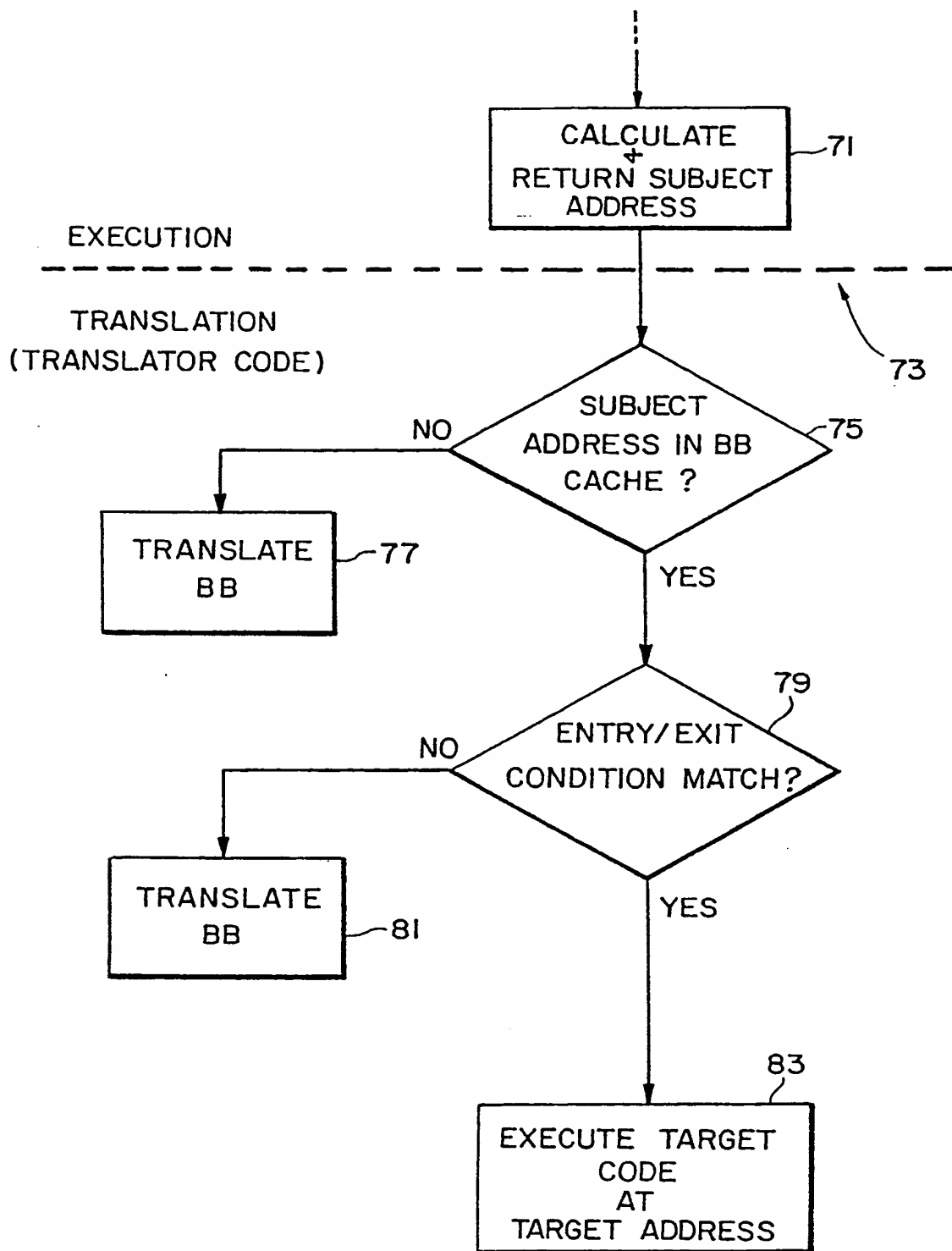


FIG. 5

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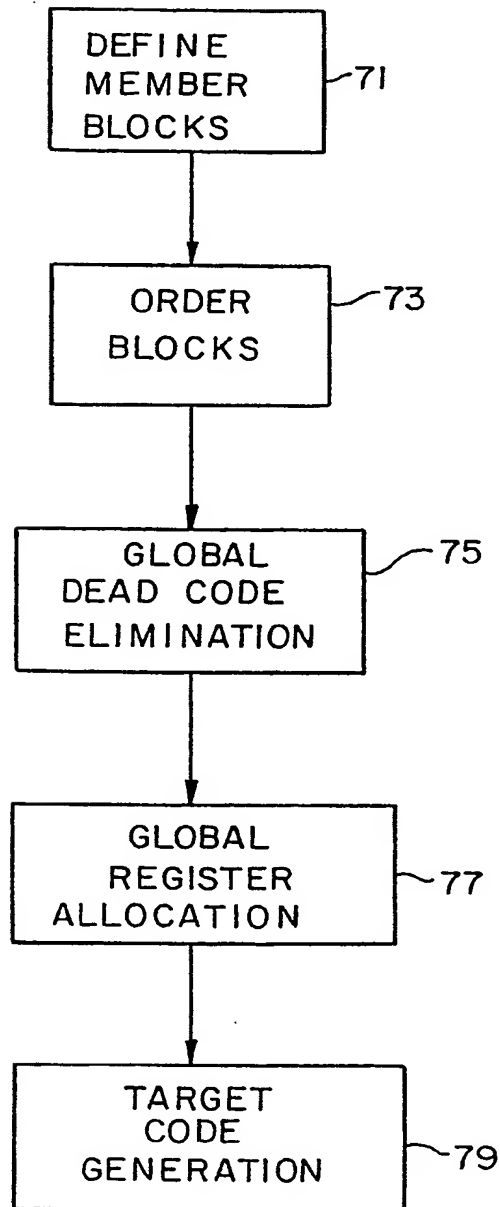


FIG. 6

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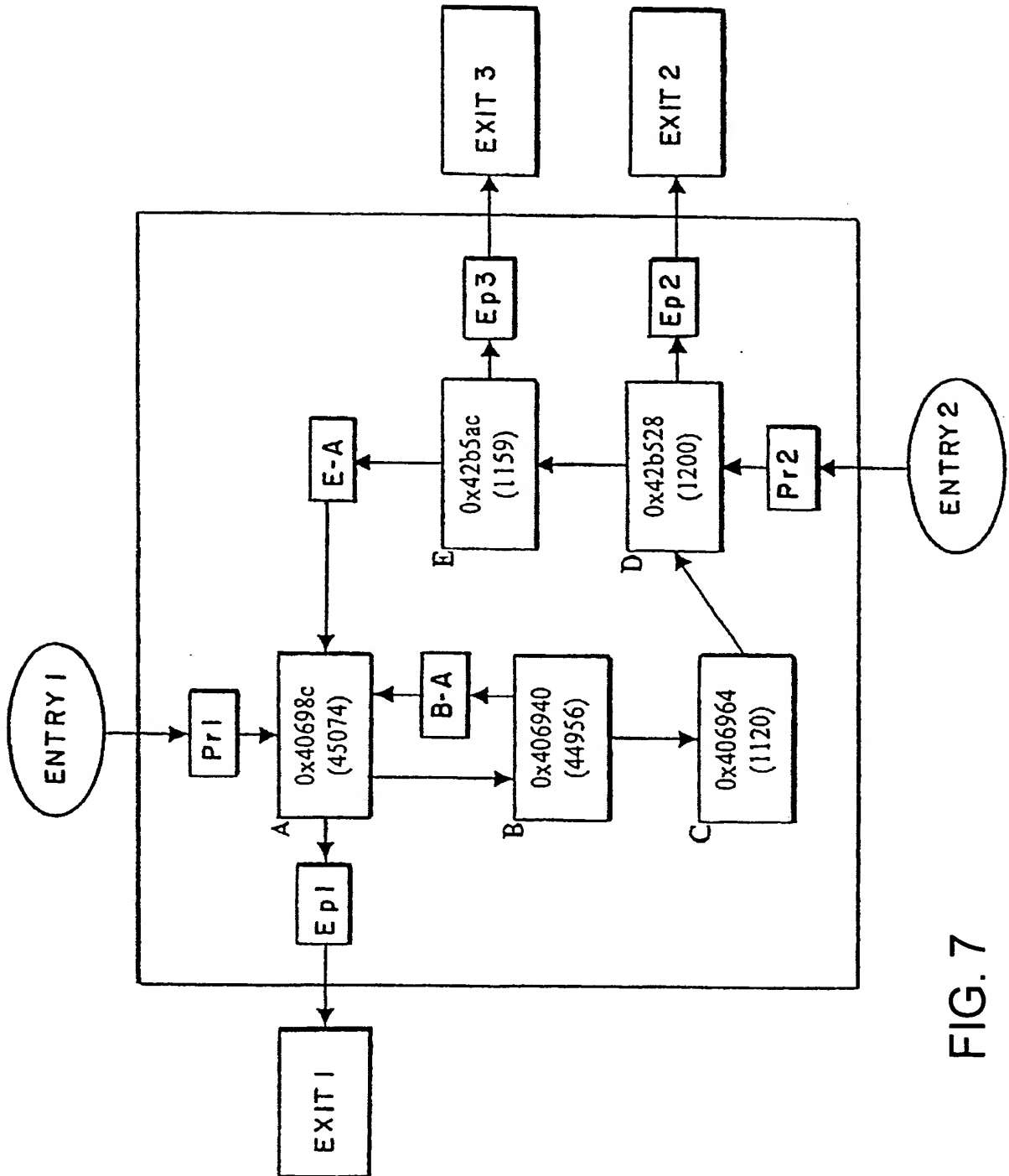


FIG. 7

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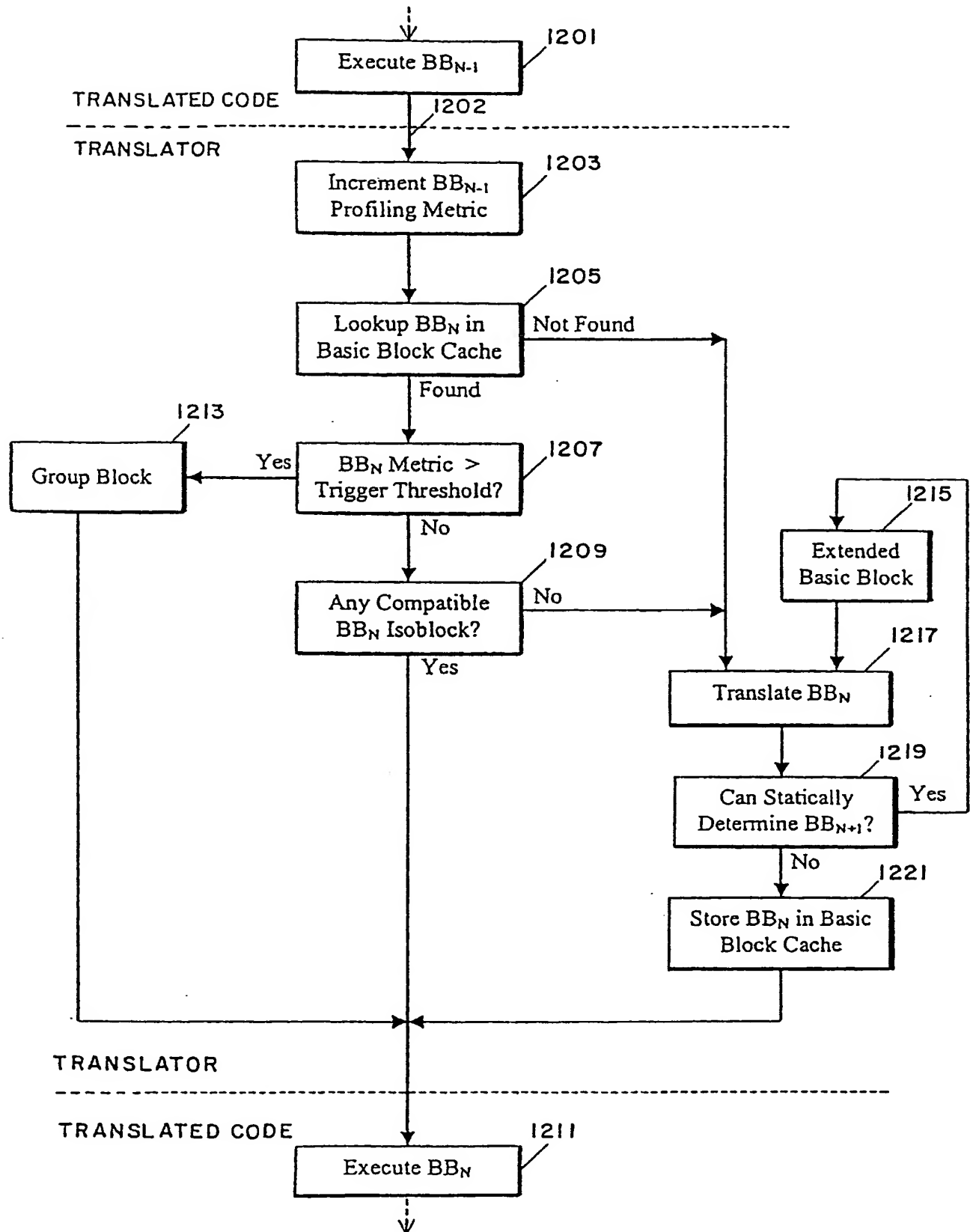


FIG. 8

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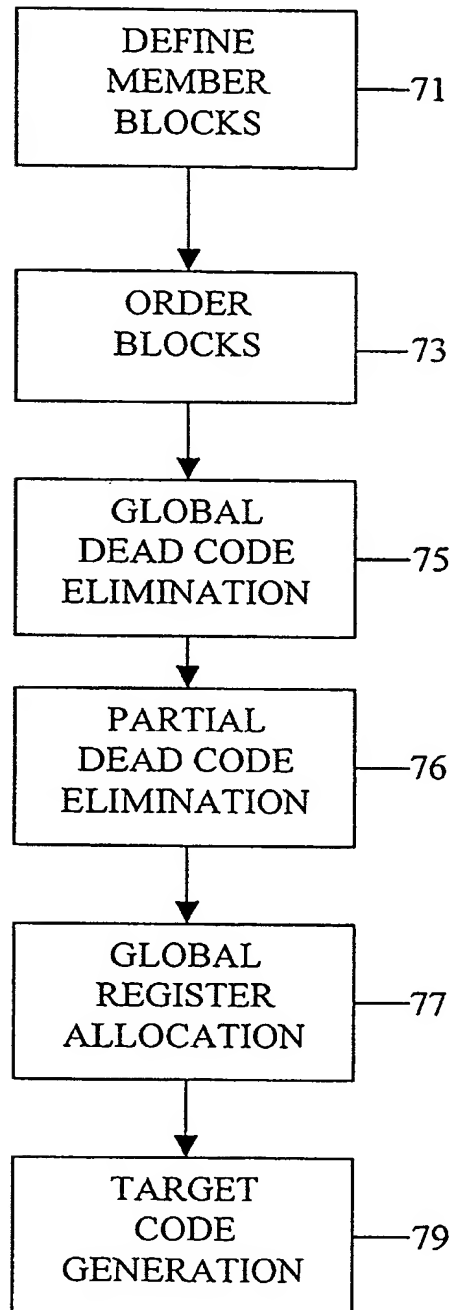


FIG. 9

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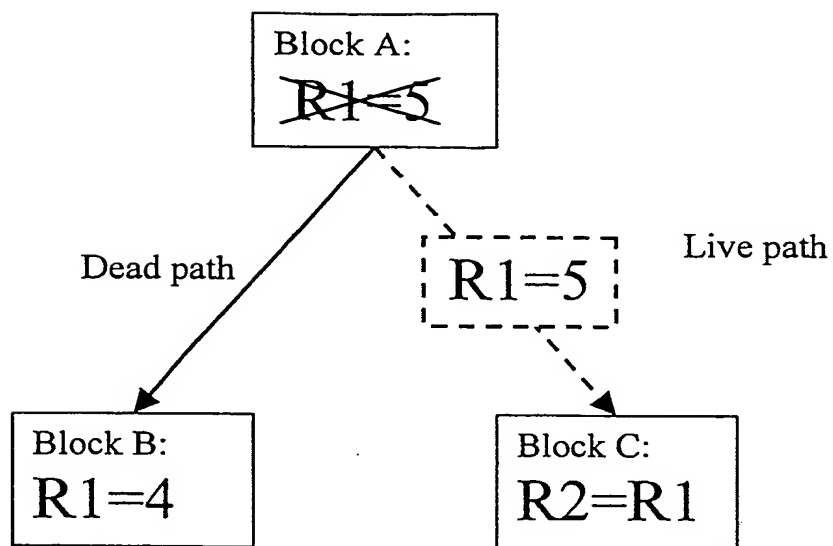


FIG. 10A

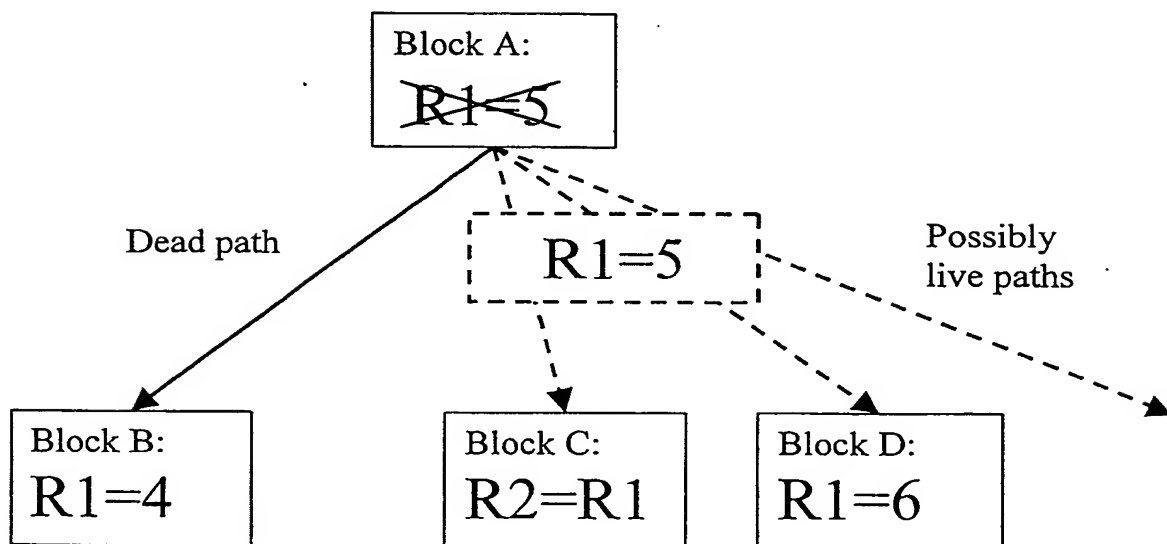


FIG. 10B

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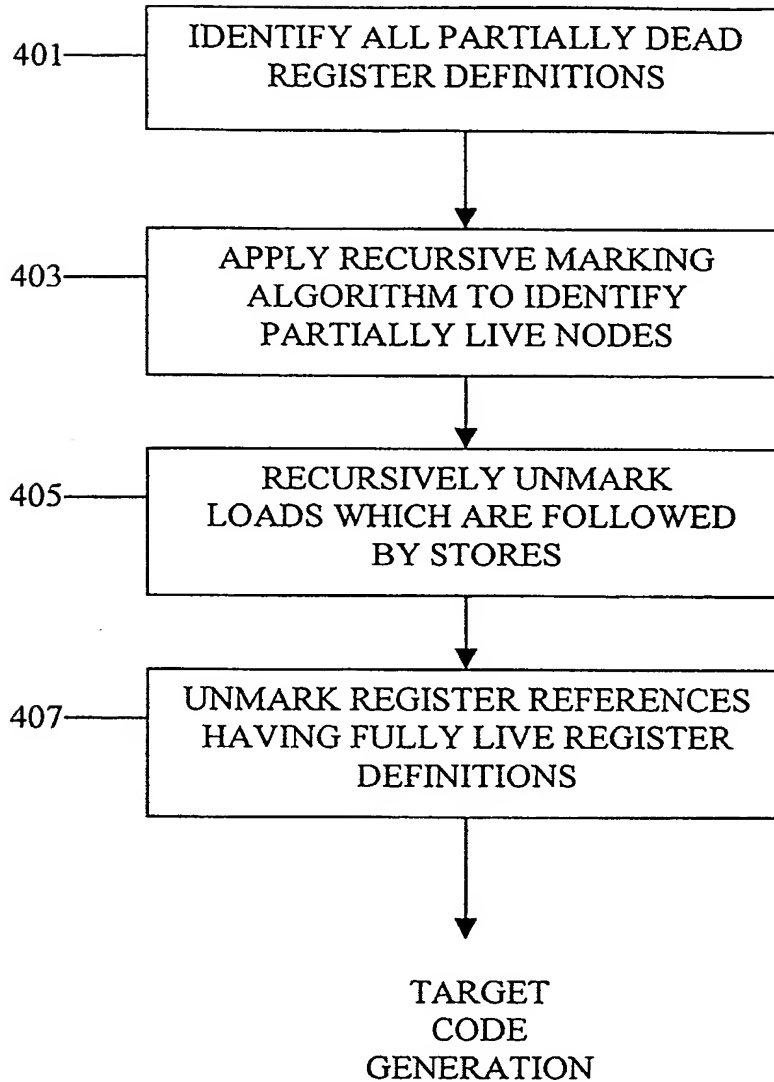


FIG. 11

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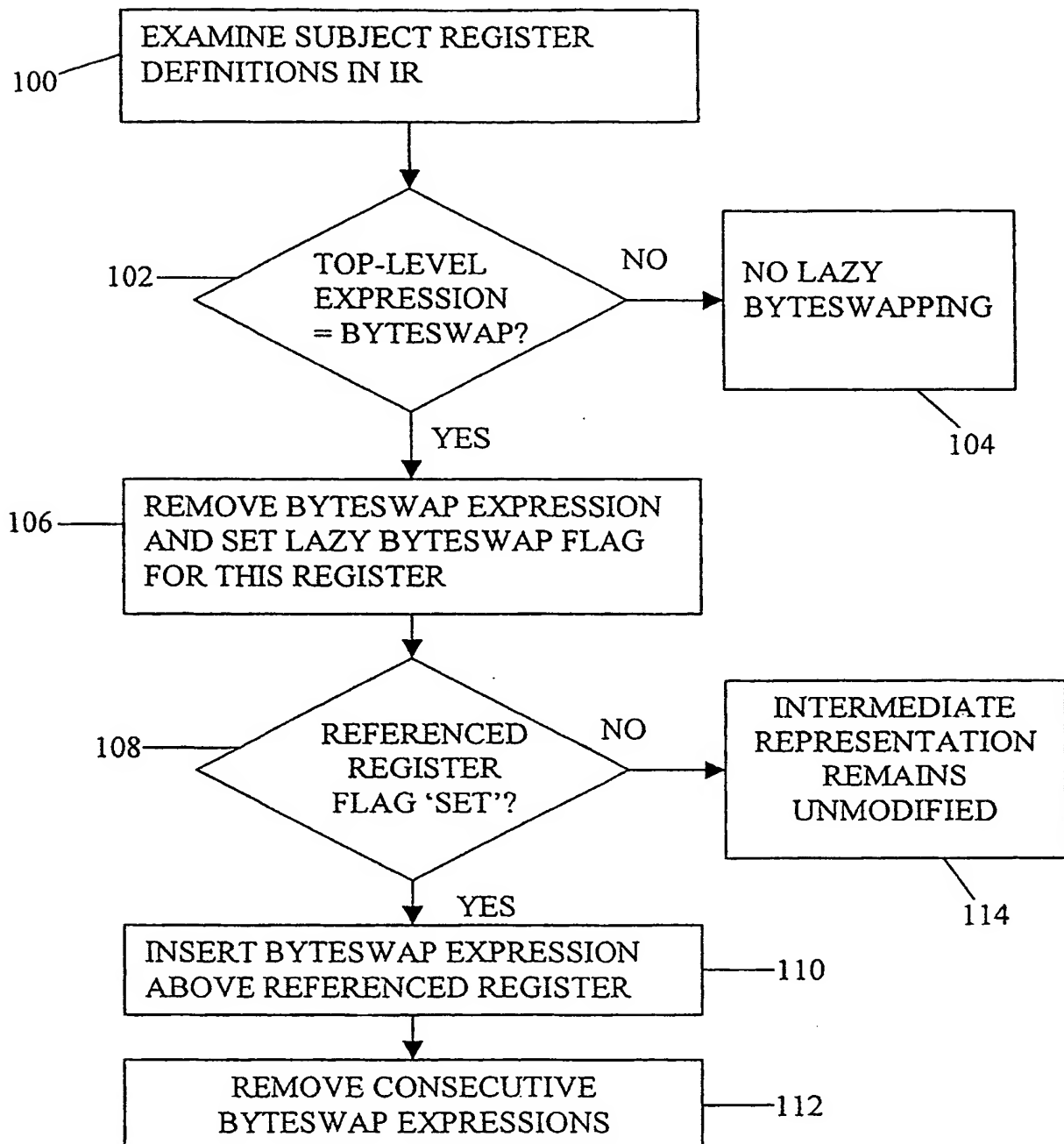


FIG. 12

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Subject Code

200

```
LABEL1    r3 := LOAD(r2 + 24)
          r1 := r1 -1
          CMP r1 0
          BC LABEL1
          STORE 16(r5) r3
```

Group Block

202

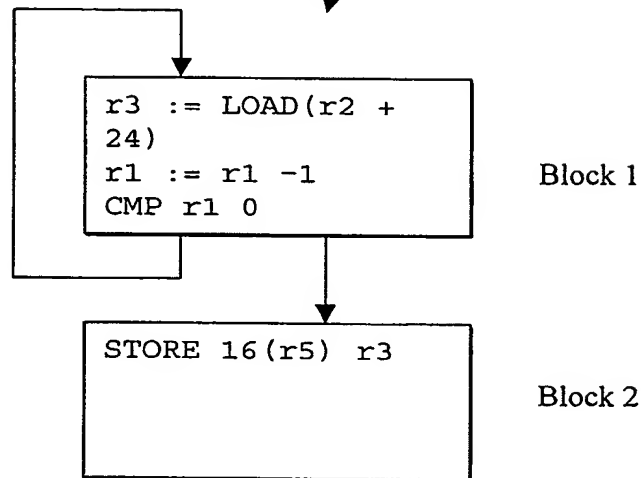
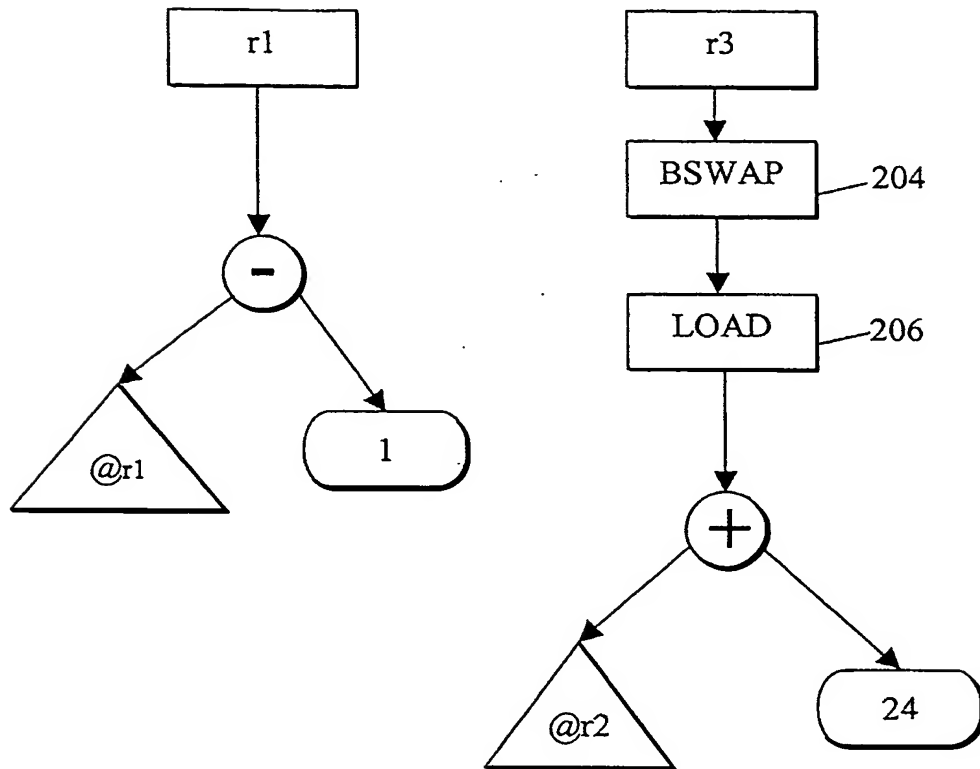


FIG. 13A

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Block 1 IR Without Lazy Byteswapping



Block 2 IR Without Lazy Byteswapping

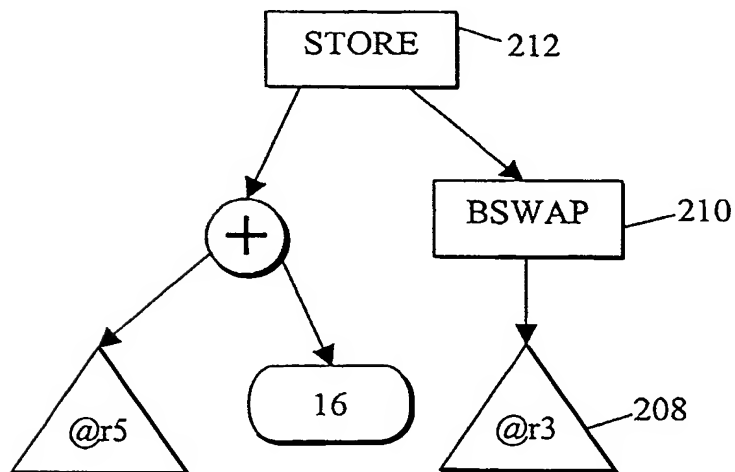


FIG. 13B

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Block 2 IR With Lazy Byteswapping

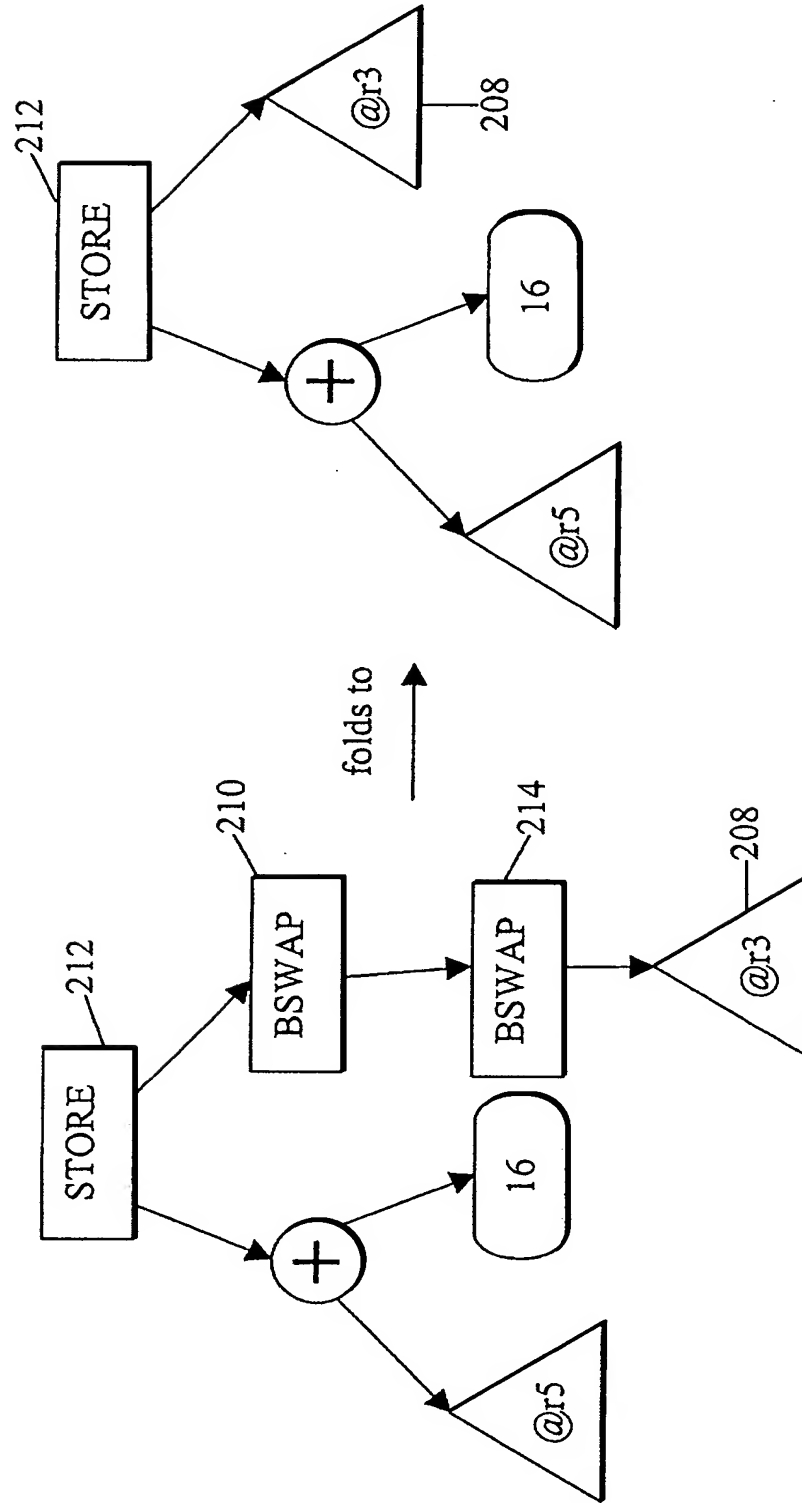


FIG. 13C

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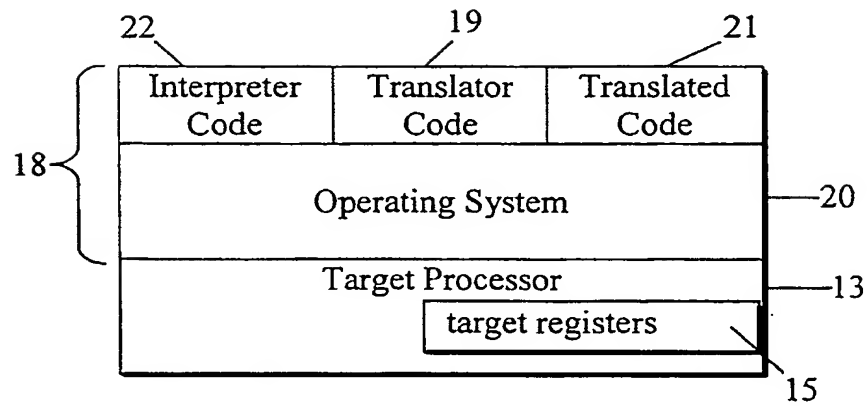


FIG. 14

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